FAIRCHILD

SEMICONDUCTOR

DM74ALS253 3-STATE Dual 1-of-4 Line Data Selector/Multiplexer

General Description

This Data Selector/Multiplexer contains full on-chip decoding to select one-of-four data sources as a result of a unique two-bit binary code at the Select Inputs. Each of the two Data Selector/Multiplexer circuits have their own separate Data and Output Control inputs and a non-inverting 3-STATE output buffer. The Output Control inputs, when at the high level, place the corresponding output in the high impedance OFF-State. In order to prevent bus access conflicts, output disable times are shorter than output enable times. The Select input buffers incorporate internal overlap features to ensure that select input changes do not cause invalid output transients.

Features

Advanced oxide-isolated, ion-implanted Schottky TTL process

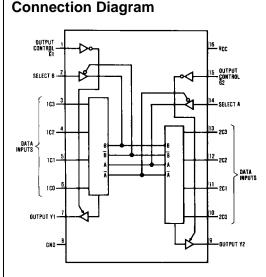
April 1984

Revised February 2000

- Switching performance is guaranteed over full temperature and V_{CC} supply range
- Pin and functional compatible with LS family counterpart
- Improved output transient handling capability
- Output control circuitry incorporates power-up 3-STATE feature

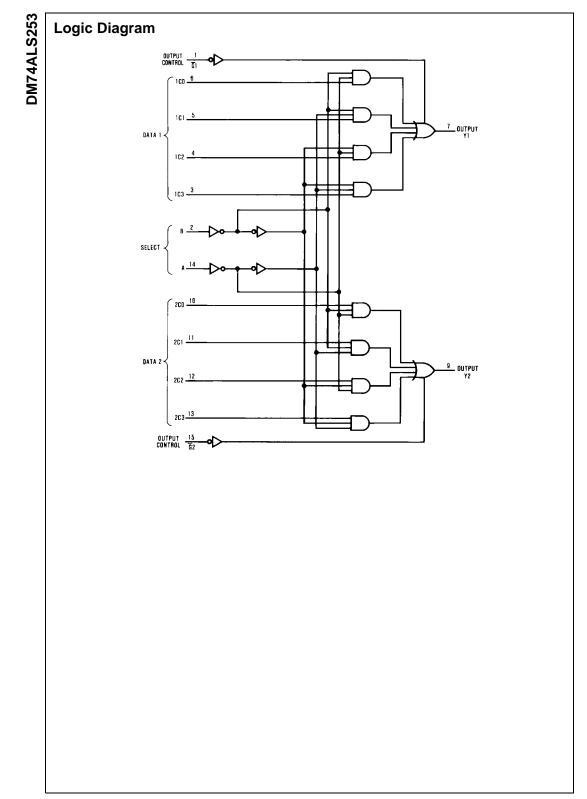
Ordering Code:

Order Number	Package Number	Package Description				
DM74ALS253M	M16A	16-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-012, 0.150 Narrow				
DM74ALS253N	N16E	16-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300 Wide				
Devices also available	Devices also available in Tape and Reel. Specify by appending the suffix letter "X" to the ordering code.					



Function Table

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Absolute Maximum Ratings(Note 1)

Supply Voltage, V _{CC}	7V
Input Voltage	7V
Voltage Applied to Disabled Output	5.5V
Operating Free Air Temperature Range	$0^{\circ}C$ to $+70^{\circ}C$
Storage Temperature Range	$-65^{\circ}C$ to $+150^{\circ}C$
Typical θ _{JA}	
N Package	78.0°C/W
M Package	107.0°C/W

Note 1: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the Electrical Characteristics tables are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

Recommended Operating Conditions

Symbol	Parameter	Min	Nom	Max	Units
V _{CC}	Supply Voltage	4.5	5	5.5	V
V _{IH}	HIGH Level Input Voltage	2			V
V _{IL}	LOW Level Input Voltage			0.8	V
он	HIGH Level Output Current			-2.6	mA
OL	LOW Level Output Current			24	mA
T _A	Free Air Operating Temperature	0		70	°C

Electrical Characteristics

over recommended operating free air temperature range. All typical values are measured at $V_{CC} = 5V$, $T_A = 25^{\circ}C$.

Symbol	Parameter	Conditions		Min	Тур	Max	Units	
V _{IK}	Input Clamp Voltage	$V_{CC} = 4.5V, I_{IN} = -18 \text{ mA}$				-1.5	V	
V _{OH}	HIGH Level	$V_{CC} = 4.5V, I_{OH} = Max$ $I_{OH} = 400 \ \mu\text{A}, V_{CC} = 4.5V \text{ to } 5.5V$		2.4	3.2		V	
	Output Voltage			$V_{CC} - 2$				
V _{OL}	LOW Level	$V_{CC} = 4.5V$	I _{OL} = 12 mA		0.25	0.4	V	
	Output Voltage		I _{OL} = 24 mA		0.35	0.5	Ň	
I _I	Input Current at Maximum input Voltage	$V_{CC} = 5.5 V, V_{IN} = 7 V$				0.1	mA	
IIH	HIGH Level Input Current	$V_{CC} = 5.5 V, V_{IN} = 2.7 V$				20	μΑ	
IIL	LOW Level Input Current	$V_{CC} = 5.5 V, V_{IN} = 0.4 V$				-0.1	mA	
I _O	Output Drive Current	$V_{CC} = 5.5 V, V_{OUT} = 2.25 V$		-30		-112	mA	
I _{OZH}	OFF-State Output Current, HIGH Bias	$V_{CC} = 5.5V, V_{OUT} = 2.7V$				20	μΑ	
I _{OZL}	OFF-State Output Current, LOW Bias	$V_{CC} = 5.5 V, V_{OUT} = 0.4 V$				-20	μΑ	
I _{CC}	Supply Current	$V_{CC} = 5.5V$	Output HIGH		6.5	12		
			Output LOW		6.5	12	mA	
			Output Disabled		7.5	14	1	

Switching Characteristics

Symbol	Parameter	Conditions	From (Input) To (Output)	Min	Max	Units
t _{PLH}	Propagation Delay Time LOW-to-HIGH Level Output	$V_{CC} = 4.5V$ to 5.5V $C_L = 50 \text{ pF}$	Select to Y	5	21	ns
t _{PHL}	Propagation Delay Time HIGH-to-LOW Level Output	$R_L=500\Omega$	Select to Y	5	21	ns
t _{PLH}	Propagation Delay Time LOW-to-HIGH Level Output		Data to Y	2	10	ns
t _{PHL}	Propagation Delay Time HIGH-to-LOW Level Output		Data to Y	3	14	ns
t _{PZH}	Output Enable Time to HIGH Level Output		Output Control to Y	3	14	ns
t _{PZL}	Output Enable Time to LOW Level Output		Output Control to Y	4	16	ns
t _{PHZ}	Output Disable Time from HIGH Level Output		Output Control to Y	2	10	ns
t _{PLZ}	Output Disable Time from LOW Level Output		Output Control to Y	2	14	ns

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